

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
SCANNED <i>Am</i> ² Q.A. <i>W</i>	

APPLICATION NO: 09/684160	CONT/PRIOR	CLASS 711	SUBCLASS	ART UNIT 2185 2172	EXAMINER Hamilton
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Method and system for generating a bit order data structure of configuration bits for a programmable logic device

PTO-2040
12/89

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER _____ _____ _____	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____	_____ (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED _____ _____	
	_____ (Primary Examiner) (Date)			ISSUE FEE <div> Amount Due Date Paid </div>	
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed. _____ _____	_____ (Legal Instruments Examiner) (Date)			ISSUE BATCH NUMBER _____ _____	

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